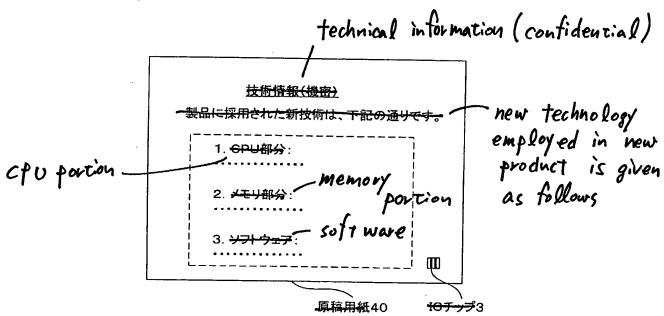
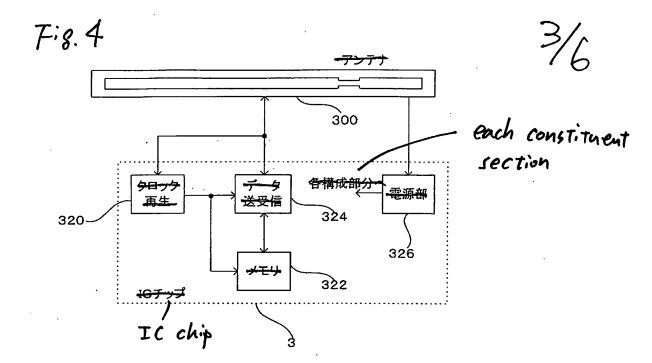


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装置本体10

1図31 Fig. o





【図5】 Fig. 5

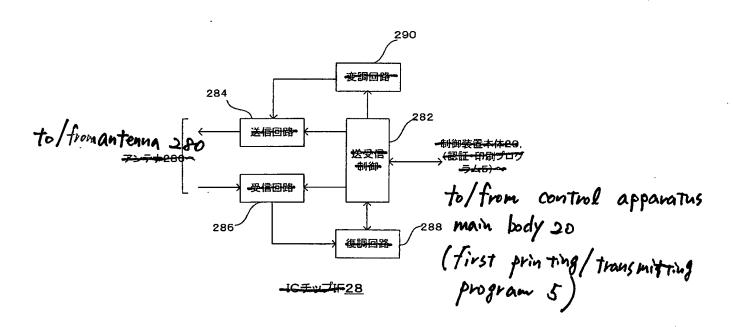
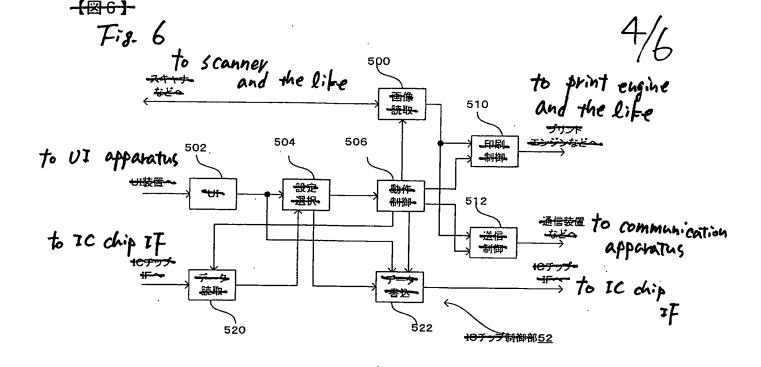


FIG. 4

- 320 clock reproducing circuit
- 322 memory circuit
- 324 data transmitting/receiving circuit
- 326 power supply circuit

FIG. 5

- 282 transmission/reception control circuit
- 284 transmission circuit
- 286 reception circuit
- 288 demodulation circuit
- 290 modulation circuit



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18.7 Fig. 7

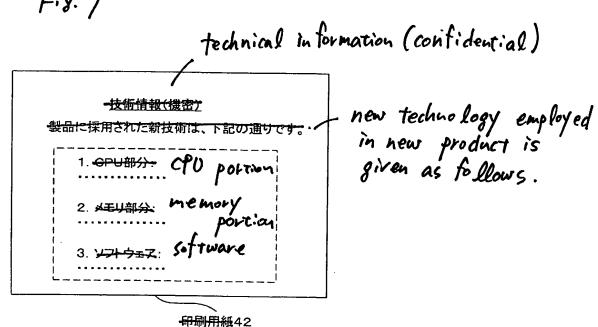
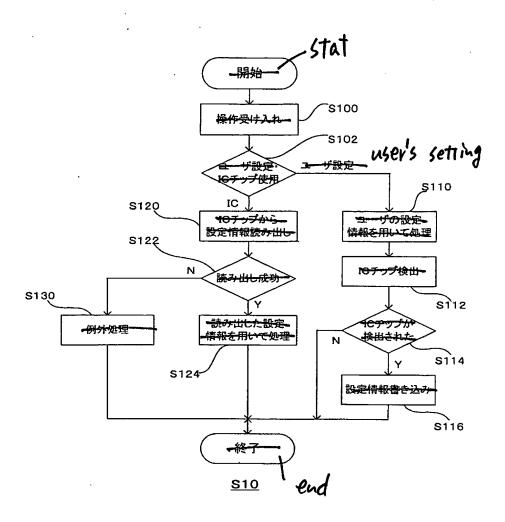
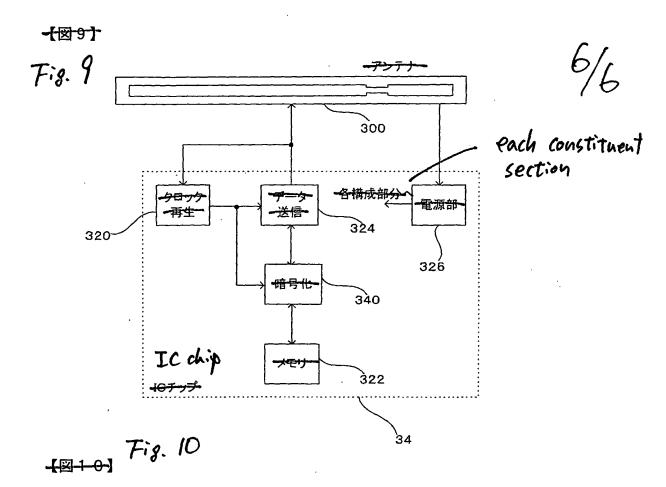


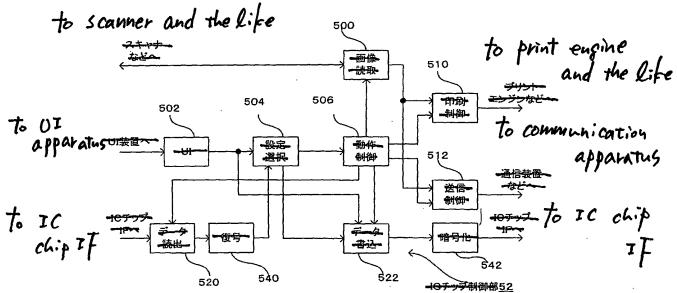
Fig. 6

- 500 image reading section
- 502 UI section
- 504 setting selection section
- 506 operation control section
- 510 print control section
- 512 transmission control section
- 520 data reading section
- 522 data writing section



- S100 accept operation
- S102 use user setting or IC chip?
- S110 execute operation by using user's setting
- information
- S112 detect IC chip
- S114 IS IC chip detected?
- S116 write setting information
- S120 read setting information from IC chip
- S122 reading succeeds?
- S124 execute process by using read setting information
- S130 execute exception process





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Fig. 9

- 320 clock reproducing circuit
- 322 memory circuit
- 324 data transmitting/receiving circuit
- 326 power supply circuit

Fig. 10

- 500 image reading section
- 502 UI section
- 504 setting selection section
- 506 operation control section
- 510 print control section
- 512 transmission control section
- 520 data reading section
- 522 data writing section
- 540 decryption section
- 542 encrypting section